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L Number	Hits	Search Text	DB	Time stamp
1	0	(screen adj resolut\$3) same (buffer adj resolution)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/10/22 11:03
2	296	(extend\$3 or enlarge\$3 or widen\$3 or lengthen\$3 or expand\$3 or broaden\$3) near7 (buffer near3 (resolution or size))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/10/22 12:23
3	140	((extend\$3 or enlarge\$3 or widen\$3 or lengthen\$3 or expand\$3 or broaden\$3) near7 (buffer near3 (resolution or size))) and (rectangle or box or boundar\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/10/22 11:54
4	0	((extend\$3 or enlarge\$3 or widen\$3 or lengthen\$3 or expand\$3 or broaden\$3) near7 (buffer near3 (resolution or size))) and (binn\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/10/22 11:54
7	0	((extend\$3 or enlarge\$3 or widen\$3 or lengthen\$3 or expand\$3 or broaden\$3) near7 (buffer near3 (resolution or size))) and (bin adj array)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/10/22 11:55
8	1312	(extend\$3 or enlarge\$3 or widen\$3 or lengthen\$3 or expand\$3 or broaden\$3) adj3 rectangle	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/10/22 12:23
9	121	(extend\$3 or enlarge\$3 or widen\$3 or lengthen\$3 or expand\$3 or broaden\$3) adj3 (bounding adj box)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/10/22 12:24
10	5	(382/173.ccls. or 382/176-177.ccls. or 382/180.ccls.) and "binning"	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/10/22 13:42
11	106	(382/173.ccls. or 382/176-177.ccls. or 382/180.ccls.) and clip\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/10/22 13:43
-	9	Doyle-Peter.in.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/05/03 16:19
-	18	Doyle-Peter-L.in.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/05/03 16:19
-	173	345/620.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/05/03 16:20

	176	(345/\$.ccls. or 382/\$.ccls. or 358/\$.ccls. or 348/\$.ccls.) and binning	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/09/09 16:26
	120	((345/\$.ccls. or 382/\$.ccls. or 358/\$.ccls. or 348/\$.ccls.) and binning) and (rectangle or square)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/09/09 15:54
	42	345/622.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/09/09 16:57
	36	345/622.ccls. not 345/620.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/05/03 16:23
	28	345/628.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/09/09 15:32
	28	345/628.ccls. not (345/622.ccls. not 345/620.ccls.)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/05/03 16:25
	42	345/642.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/05/03 16:27
	56	((345/\$.ccls. or 382/\$.ccls. or 358/\$.ccls. or 348/\$.ccls.) and binning) and (rectangle or square) and buffer	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/09/09 15:45
	120	((345/\$.ccls. or 382/\$.ccls. or 358/\$.ccls. or 348/\$.ccls.) and binning) not ((345/\$.ccls. or 382/\$.ccls. or 358/\$.ccls. or 348/\$.ccls.) and binning) and (rectangle or square) and buffer)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/09/09 15:56
	6	345/620.ccls. and (binning or bin)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/09/09 15:57
	22	(345/\$.ccls. or 382/\$.ccls. or 358/\$.ccls. or 348/\$.ccls.) and binning and clip\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/09/09 16:45
	28	(345/\$.ccls. or 382/\$.ccls. or 348/\$.ccls. or 358/\$.ccls.) and (zone adj render\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/05/03 16:27

	21	345/624.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/09/09 16:57
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	2	6344852.pn.	USPAT; US-PGPUB; DERWENT	2003/09/10 08:56
	2	6326964.pn.	USPAT; US-PGPUB; DERWENT	2003/09/10 15:26
	2	4888712.pn.	USPAT; US-PGPUB; DERWENT	2004/05/03 15:45
	2	6222550.pn.	USPAT; US-PGPUB; DERWENT	2003/09/10 15:27
	2	6166748.pn.	USPAT; US-PGPUB; DERWENT	2003/09/10 15:27
	2	6154223.pn.	USPAT; US-PGPUB; DERWENT	2003/09/10 15:28
	2	6104415.pn.	USPAT; US-PGPUB; DERWENT	2003/09/10 15:28
	2	6034699.pn.	USPAT; US-PGPUB; DERWENT	2003/09/10 15:29
	2	5953506.pn.	USPAT; US-PGPUB; DERWENT	2003/09/10 15:29
	3	5936642.pn.	USPAT; US-PGPUB; DERWENT	2003/09/10 15:29

	15	(US-6362826-\$ or US-6518976-\$ or US-6437780-\$ or US-6377276-\$ or US-6172682-\$ or US-6144387-\$ or US-6437796-\$ or US-4811241-\$ or US-5473740-\$ or US-4982345-\$ or US-6525726-\$ or US-6344852-\$ or US-6424345-\$ or US-5850475-\$).did. or (US-20020140710-\$).did.	USPAT; US-PPGPUB	2003/09/10 16:42
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	3	3816726.pn.	USPAT; US-PPGPUB; DERWENT	2004/05/03 15:46
	18	Doyle-Peter-L.in.	USPAT; US-PPGPUB; EPO; JPO; DERWENT	2004/10/21 12:33
	14	Doyle-Peter.in.	USPAT; US-PPGPUB; EPO; JPO; DERWENT	2004/05/03 16:19
	191	345/620.ccis.	USPAT; US-PPGPUB; EPO; JPO; DERWENT	2004/10/21 12:33
	45	345/622.ccis. not 345/620.ccis.	USPAT; US-PPGPUB; EPO; JPO; DERWENT	2004/10/21 12:33
	30	345/628.ccis. not (345/622.ccis. not 345/620.ccis.)	USPAT; US-PPGPUB; EPO; JPO; DERWENT	2004/10/21 12:33
	43	345/642.ccis.	USPAT; US-PPGPUB; EPO; JPO; DERWENT	2004/10/21 12:33
	32	(345/\$.ccis. or 382/\$.ccis. or 348/\$.ccis. or 358/\$.ccis.) and (zone adj render\$3)	USPAT; US-PPGPUB; EPO; JPO; DERWENT	2004/10/21 12:33
	198	345/620.ccis.	USPAT; US-PPGPUB; EPO; JPO; DERWENT	2004/10/21 12:33

	25	Doyle-Peter-L.in.	USPAT; US-PPGPUB; EPO; JPO; DERWENT USPAT	2004/10/21 12:34
	5	("6344852" "6384833" "6469704" "6557083" "6567084").PN.		2004/10/21 12:58
	50	345/622.ccls. not 345/620.ccls.	USPAT; US-PPGPUB; EPO; JPO; DERWENT USPAT	2004/10/21 12:38
	31	345/628.ccls. not (345/622.ccls. not 345/620.ccls.)	USPAT; US-PPGPUB; EPO; JPO; DERWENT USPAT	2004/10/21 12:38
	43	345/642.ccls.	USPAT; US-PPGPUB; EPO; JPO; DERWENT USPAT	2004/10/21 12:38
	39	(345/\$.ccls. or 382/\$.ccls. or 348/\$.ccls. or 358/\$.ccls.) and (zone adj render\$3)	USPAT; US-PPGPUB; EPO; JPO; DERWENT USPAT	2004/10/21 12:38
	8	("5255352" "5317682" "5680525" "5886701" "6169549" "6219058" "6269175" "6563501").PN.	USPAT	2004/10/21 12:42
	34	345/620-629.ccls. and (bin\$4 near7 (screen or display))	USPAT; US-PPGPUB; EPO; JPO; DERWENT USPAT	2004/10/21 13:00
	3	("6344852" "6380935" "6437780" "2003/0122815").PN.	USPAT	2004/10/21 13:01
	15	(US-6437796-\$ or US-4811241-\$ or US-5473740-\$ or US-4982345-\$ or US-5850475-\$ or US-6525726-\$ or US-6344852-\$ or US-6362826-\$ or US-6437780-\$ or US-6518976-\$ or US-6377276-\$ or US-6172682-\$ or US-6144387-\$ or US-6424345-\$).did. or (US-20020140710-\$).did.	USPAT; US-PPGPUB	2004/10/21 13:28
	0	((US-6437796-\$ or US-4811241-\$ or US-5473740-\$ or US-4982345-\$ or US-5850475-\$ or US-6525726-\$ or US-6344852-\$ or US-6362826-\$ or US-6437780-\$ or US-6518976-\$ or US-6377276-\$ or US-6172682-\$ or US-6144387-\$ or US-6424345-\$).did. or (US-20020140710-\$).did.) and ((extend\$3 or enlarge\$3 or widen\$3 or lengthen\$3 or expand\$3 or broaden\$3) near5 (box or rectangle or boundary))	USPAT; US-PPGPUB; DERWENT	2004/10/21 13:38

	9	345/622.ccIs. and ((extend\$3 or enlarge\$3 or widen\$3 or lengthen\$3 or expand\$3 or broaden\$3) near5 (box or rectangle or boundary))	USPAT; US-PPGPUB; DERWENT	2004/10/21 13:42
	1	345/621.ccIs. and ((extend\$3 or enlarge\$3 or widen\$3 or lengthen\$3 or expand\$3 or broaden\$3) near5 (box or rectangle or boundary))	USPAT; US-PPGPUB; DERWENT	2004/10/21 13:42
	19	345/620.ccIs. and ((extend\$3 or enlarge\$3 or widen\$3 or lengthen\$3 or expand\$3 or broaden\$3) near5 (box or rectangle or boundary))	USPAT; US-PPGPUB; DERWENT	2004/10/21 14:00
	2	345/623.ccIs. and ((extend\$3 or enlarge\$3 or widen\$3 or lengthen\$3 or expand\$3 or broaden\$3) near5 (box or rectangle or boundary))	USPAT; US-PPGPUB; DERWENT	2004/10/21 14:00
	27	345/624-629.ccIs. and ((extend\$3 or enlarge\$3 or widen\$3 or lengthen\$3 or expand\$3 or broaden\$3) near5 (box or rectangle or boundary))	USPAT; US-PPGPUB; DERWENT	2004/10/21 14:06
	0	345/624-629.ccIs. and ((extend\$3 or enlarge\$3 or widen\$3 or lengthen\$3 or expand\$3 or broaden\$3) near7 (binn\$3))	USPAT; US-PPGPUB; DERWENT	2004/10/21 14:01
	0	382/\$.ccIs. and ((extend\$3 or enlarge\$3 or widen\$3 or lengthen\$3 or expand\$3 or broaden\$3) near7 (binn\$3))	USPAT; US-PPGPUB; DERWENT	2004/10/21 14:02
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	1	345/620-629.ccIs. and ((extend\$3 or enlarge\$3 or widen\$3 or lengthen\$3 or expand\$3 or broaden\$3) near7 (binn\$3))	USPAT; US-PPGPUB; DERWENT	2004/10/21 14:02
	4	345/620-629.ccIs. and (buffer adj resolution)	USPAT; US-PPGPUB; DERWENT	2004/10/21 14:07
	1	345/\$.ccIs. and (buffer adj resolution) and binn\$3	USPAT; US-PPGPUB; DERWENT	2004/10/21 14:07
	2	345/\$.ccIs. and (buffer near3 resolution) and binn\$3	USPAT; US-PPGPUB; DERWENT	2004/10/21 14:08
	345	345/\$.ccIs. and (buffer near3 resolution)	USPAT; US-PPGPUB; DERWENT	2004/10/21 14:08
	45	345/\$.ccIs. and (buffer adj resolution)	USPAT; US-PPGPUB; DERWENT	2004/10/21 14:17
	3	"6693637"	USPAT; US-PPGPUB; DERWENT	2004/10/21 14:17

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No. Publication No.

Title

1. <u>2003 - 234030</u>	METHOD AND APPARATUS FOR ASSEMBLING JUNCTION BOX WITH WIRE HARNESS
2. <u>2003 - 044875</u>	THREE-DIMENSIONAL DATA PROCESSOR, PROGRAM, RECORDING MEDIUM, AND THREE-DIMENSIONAL DATA PROCESSING METHOD
3. <u>2002 - 096948</u>	METHOD OF BINDING OR STORING PAPER MACHINE WIRE
4. <u>2001 - 253339</u>	CABLEWAY CARRYING DEVICE
5. <u>2001 - 080615</u>	ANNULAR BINDER SEPARATING DEVICE
6. <u>2001 - 032553</u>	OPERATING BOX STRUCTURE OF PARKING DEVICE
7. <u>11 - 305332(1999)</u>	SUB-MIRROR DRIVING DEVICE FOR SINGLE-LENS REFLEX CAMERA
8. <u>11 - 291956(1999)</u>	AUXILIARY EQUIPMENT FOR OPENING AND CLOSING OF VEHICLE GATE
9. <u>11 - 100024(1999)</u>	CORRUGATED FIBERBOARD BOX
10. <u>10 - 203223(1998)</u>	AUTOMOBILE INSPECTION CERTIFICATE/TOOL HOUSING CASE OPENED/CLOSED ON STEERING WHEEL AND USED ALSO AS EASY TABLE
11. <u>09 - 184928(1997)</u>	OPTICAL FIBER CABLE TERMINATION BOX
12. <u>08 - 295332(1996)</u>	BOX PREVENTING ITS UNJUST UNSEALING
13. <u>08 - 142693(1996)</u>	INSTALLATION STRUCTURE OF CANISTER

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No. Publication No.

Title

1. 09 - 094308(1997) BUFFER BOARD FOR SPORTS
2. 08 - 028016(1996) FLOOR UNDERLAY MATERIAL
3. 63 - 005476(1988) DISPLAY DEVICE FOR OPERATING RECTANGLE
4. 62 - 112149(1987) EXPOSING DEVICE FOR PORTABLE COPYING DEVICE

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RESULT LISTApproximately **242** results found in the Worldwide database for:**(extend OR expand) AND bounding** in the title or abstract

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1 Plastics panel**Inventor:** SAUNDERS JOHN FREDERICK (GB)**Applicant:** NORFOLK GREENHOUSES LTD (GB)**EC:****IPC:** E06B3/68 ; E04D3/32**Publication info:** **GB2399379** - 2004-09-15**2 Gamma ray collimator build up****Inventor:** ARNDT JUERGEN (SE); NILSSON BOERJE (SE)**Applicant:** ELEKTA AB (SE)**EC:** G21K1/02B**IPC:** B23P15/16**Publication info:** **US6763588** - 2004-07-20**3 Improvements in Flying Machines.****Inventor:** MARKS EDWARD CHARLES ROBERT (GB)**Applicant:** MARKS EDWARD CHARLES ROBERT (GB)**EC:** B64C3/38B**IPC:****Publication info:** **GB191127907** - 1912-12-05**4 Bounding Top.****Inventor:** GRIFFIN WILLIAM (US)**Applicant:** GRIFFIN WILLIAM (US)**EC:****IPC:****Publication info:** **GB190403697** - 1904-03-31**5 CONSTRUCTIONAL UNIT****Inventor:** BLACK JOHN ALEXANDER (GB); BLACK ROBERT DAVID (GB)**Applicant:** BLACK JOHN ALEXANDER (GB); BLACK ROBERT DAVID (GB)**EC:** E04G1/36; E04G21/32B**IPC:** E04G21/32 ; E04G1/36**Publication info:** **WO2004035961** - 2004-04-29**6 Silencer****Inventor:** WEINHOLD PETER (DE); WINTRICH PETER (DE)**Applicant:** 3W MODELMOTOREN GMBH WEINHOLD (DE)**EC:** F01N1/08F; F01N1/08H3**IPC:** F01N1/08**Publication info:** **EP1380730** - 2004-01-14**7 PREMIXING FUEL INJECTOR AND METHOD OF OPERATION****Inventor:** KENDRICK DONALD W (US); SOWA WILLIAM **Applicant:** UNITED TECHNOLOGIES CORP (US) A (US); (+1)**EC:** **IPC:** F23D14/62 ; F23C7/00 ; (+3)**Publication info:** **CA2272112** - 1999-11-18**8 BRAKING BAND FOR A SLIDING DISC BRAKE****Inventor:** TIRONI GIOVANNI MARIO (IT); GOTTI GIOVANNI (IT); (+1)**Applicant:** TIRONI GIOVANNI MARIO (IT); GOTTI GIOVANNI (IT); (+2)**EC:** F16D55/26; F16D65/12D; (+1)**IPC:** F16D65/12 ; F16D65/847**Publication info:** **WO03062661** - 2003-07-31**9 DEVICE FOR THE FORMATION OF A CONNECTING TRANSITION BETWEEN TWO PERPENDICULARLY ADJOINING SURFACES****Inventor:** SCHLUETER WERNER (DE)**Applicant:** SCHLUETER WERNER (DE)**EC:****IPC:** E04F19/04**Publication info:** **CA1289330** - 1991-09-24**10 MECHANISM FOR SUPPORTING AND EXTENDING A HIGH LIFT DEVICE FOR AIRCRAFT WINGS****Inventor:** WILLIAMS GEORGE H (GB)**Applicant:** WILLIAMS GEORGE H (GB)**EC:****IPC:** B64C9/24 ; B64C13/34**Publication info:** **CA1285922** - 1991-07-09

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13 results found in the Worldwide database for:
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1 Improvement in machine for making inlaid linoleum

Inventor: Applicant: THOMAS PESCI

EC: D06N7/00B4 IPC:

Publication info: **GB669937** - 1952-04-09

2 Improvements in or relating to bearings, oil seals and the like

Inventor: Applicant: ALEXANDER DRYSDALE

EC: F16J15/20 IPC:

Publication info: **GB657592** - 1951-09-19

3 Improvements in and relating to a logarithmic periodic antenna

Inventor: Applicant: TEWEA TECH WETENSCHAPPELIJKE A

EC: H01Q11/10 IPC:

Publication info: **GB984559** - 1965-02-24

4 Photographic processing apparatus

Inventor: Applicant: OCE VAN DER GRINTEN NV

EC: G03B27/30; G03B27/30G IPC:

Publication info: **GB1127459** - 1968-09-18

5 A box

Inventor: WEBB HUGH (GB) Applicant: FOLDERS GALORE LTD (GB)

EC: B42F7/14; B65D5/18; (+1) IPC: B65D5/18 ; B42F7/14

Publication info: **GB2351489** - 2001-01-03

6 Flexibly mounted sealing strips of a vacuum roll for a web dryer

Inventor: GRIMM HELMUT (DE); KAHL PETER (DE); Applicant: VOITH GMBH J M (DE)

(+2) IPC: F26B13/04

Publication info: **US5515619** - 1996-05-14

7 Apparatus for continuously crimping thermoplastic threads

Inventor: NABULON WERNER (CH) Applicant: RIETER AG MASCHF (CH)

EC: D02G1/12; D02G1/12B IPC: D02G1/12

Publication info: **US5365644** - 1994-11-22

8 Fluidised bed furnaces

Inventor: Applicant: WORSLEY G P & CO LTD

EC: B01J8/44; F23C10/18 IPC: F23C11/02

Publication info: **GB2086754** - 1982-05-19

9 Improvements in or relating to cooling arrangements for dynamo electrical machines

Inventor: Applicant: OTTO TITUSZ BLATHY

EC: H02K1/20; H02K9/19 IPC:

Publication info: **GB293590** - 1928-07-12

10 Flushing-device.

Inventor: MENGE HEINRICH (DE) Applicant: MENGE HEINRICH (DE)

EC: E03D1/14D IPC: E03D1/14

Publication info: **EP0529237** - 1993-03-03

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3 results found in the Worldwide database for:
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1 Low profile display

Inventor: HOUSMAN RICHARD W

Applicant: HOUSMAN RICHARD W

EC: B65D5/02K; B65D5/52B; (+2)

IPC: B65D85/20

Publication info: **US4232799** - 1980-11-11

2 COMBINATION DEPALLETIZER AND PALLETIZER

Inventor: GAL GEORGE E VON JR; HUTCHINSON

Applicant: GEORGE E VON GAL JR; LAWRENCE A

LAWRENCE A; (+1)

HUTCHINSON; (+1)

EC: B65G59/00B; B65G60/00

IPC: B65G59/04

Publication info: **US3682338** - 1972-08-08

3 VENDING DEVICE

Inventor: HOUSMAN RICHARD W

Applicant: HOUSMAN RICHARD W

EC:

IPC: B65D85/20 ; B65D85/20

Publication info: **CA1141718** - 1983-02-22

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1 Low profile display

Inventor: HOUSMAN RICHARD W

Applicant: HOUSMAN RICHARD W

EC: B65D5/02K; B65D5/52B; (+2)

IPC: B65D85/20

Publication info: **US4232799** - 1980-11-11

2 COMBINATION DEPALLETIZER AND PALLETIZER

Inventor: GAL GEORGE E VON JR; HUTCHINSON

Applicant: GEORGE E VON GAL JR; LAWRENCE A

LAWRENCE A; (+1)

HUTCHINSON; (+1)

EC: B65G59/00B; B65G60/00

IPC: B65G59/04

Publication info: **US3682338** - 1972-08-08

3 VENDING DEVICE

Inventor: HOUSMAN RICHARD W

Applicant: HOUSMAN RICHARD W

EC:

IPC: B65D85/20 ; B65D85/20

Publication info: **CA1141718** - 1983-02-22

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Relevance scale **1 Computing curricula 2001**September 2001 **Journal on Educational Resources in Computing (JERIC)**Full text available:  [pdf\(613.63 KB\)](#)Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#) [html\(2.78 KB\)](#)**2 A general framework for prefetch scheduling in linked data structures and its application to multi-chain prefetching**

Seungryul Choi, Nicholas Kohout, Sumit Pamnani, Dongkeun Kim, Donald Yeung

May 2004 **ACM Transactions on Computer Systems (TOCS)**, Volume 22 Issue 2Full text available:  [pdf\(2.45 MB\)](#)Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#) [html](#)

Pointer-chasing applications tend to traverse composite data structures consisting of multiple independent pointer chains. While the traversal of any single pointer chain leads to the serialization of memory operations, the traversal of independent pointer chains provides a source of memory parallelism. This article investigates exploiting such *interchain memory parallelism* for the purpose of memory latency tolerance, using a technique called *multi-chain prefetching*. Previous work ...

Keywords: Data prefetching, memory parallelism, pointer-chasing code**3 Dissertation Abstracts in Computer Graphics**

Jeffrey J. McConnell

April 1988 **ACM SIGGRAPH Computer Graphics**, Volume 22 Issue 2Full text available:  [pdf\(1.65 MB\)](#)Additional Information: [full citation](#), [abstract](#) [html](#)

This paper is the first of a yearly compendium of abstracts from masters and doctoral theses in computer graphics. The compendium is being provided as a guide to the work being done in computer graphics by graduate students. Any requests for further information about a thesis or graduate student should be directed to the institution involved. This year's compendium has 54 entries from 23 institutions. The list is by no means complete as there are several prominent graduate schools missing. It is ...

4 Status report of the graphic standards planning committee

Computer Graphics staff

August 1979 **ACM SIGGRAPH Computer Graphics**, Volume 13 Issue 3Full text available:  [pdf\(15.01 MB\)](#)Additional Information: [full citation](#), [references](#), [citations](#) [html](#)**5 External memory algorithms and data structures: dealing with**

massive data

Jeffrey Scott Vitter

June 2001 **ACM Computing Surveys (CSUR)**, Volume 33 Issue 2

Full text available:  [pdf\(828.46 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

Data sets in large applications are often too massive to fit completely inside the computers internal memory. The resulting input/output communication (or I/O) between fast internal memory and slower external memory (such as disks) can be a major performance bottleneck. In this article we survey the state of the art in the design and analysis of external memory (or EM) algorithms and data structures, where the goal is to exploit locality in order to reduce the I/O costs. We consider a varie ...

Keywords: B-tree, I/O, batched, block, disk, dynamic, extendible hashing, external memory, hierarchical memory, multidimensional access methods, multilevel memory, online, out-of-core, secondary storage, sorting

6 Status report of the graphic standards planning committee of ACM/SIGGRAPH: State-of-the-art of graphic software packages 

Computer Graphics staff

September 1977 **ACM SIGGRAPH Computer Graphics**, Volume 11 Issue 3

Full text available:  [pdf\(9.03 MB\)](#) Additional Information: [full citation](#), [references](#)

7 Composite model-checking: verification with type-specific symbolic representations 

Tevfik Bultan, Richard Gerber, Christopher League

January 2000 **ACM Transactions on Software Engineering and Methodology (TOSEM)**, Volume 9 Issue 1

Full text available:  [pdf\(400.17 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

There has been a surge of progress in automated verification methods based on state exploration. In areas like hardware design, these technologies are rapidly augmenting key phases of testing and validation. To date, one of the most successful of these methods has been symbolic model-checking, in which large finite-state machines are encoded into compact data structures such as Binary Decision Diagrams (BDDs), and are then checked for safety and liveness properties. However, these technique ...

Keywords: Presburger arithmetic, binary decision diagrams, symbolic model-checking

8 Draft Proposed: American National Standard—Graphical Kernel System 

Technical Committee X3H3 - Computer Graphics

February 1984 **ACM SIGGRAPH Computer Graphics**, Volume 18 Issue 5

Full text available:  [pdf\(16.07 MB\)](#) Additional Information: [full citation](#)

9 Distributed systems - programming and management: On remote procedure call 

Patrícia Gomes Soares

November 1992 **Proceedings of the 1992 conference of the Centre for Advanced Studies on Collaborative research - Volume 2**

Full text available:  [pdf\(4.52 MB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#)

The Remote Procedure Call (RPC) paradigm is reviewed. The concept is described, along with the backbone structure of the mechanisms that support it. An overview of works in supporting these mechanisms is discussed. Extensions to the paradigm that have been proposed to enlarge its suitability, are studied. The main contributions of this paper are a standard view and classification of RPC mechanisms according to different perspectives, and

a snapshot of the paradigm in use today and of goals for t ...

10 Multidimensional access methods

Volker Gaede, Oliver Günther

June 1998 **ACM Computing Surveys (CSUR)**, Volume 30 Issue 2

Full text available:  [pdf\(1.05 MB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

Search operations in databases require special support at the physical level. This is true for conventional databases as well as spatial databases, where typical search operations include the point query (find all objects that contain a given search point) and the region query (find all objects that overlap a given search region). More than ten years of spatial database research have resulted in a great variety of multidimensional access methods to support ...

Keywords: data structures, multidimensional access methods

11 Fast detection of communication patterns in distributed executions

Thomas Kunz, Michiel F. H. Seuren

November 1997 **Proceedings of the 1997 conference of the Centre for Advanced Studies on Collaborative research**

Full text available:  [pdf\(4.21 MB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Understanding distributed applications is a tedious and difficult task. Visualizations based on process-time diagrams are often used to obtain a better understanding of the execution of the application. The visualization tool we use is Poet, an event tracer developed at the University of Waterloo. However, these diagrams are often very complex and do not provide the user with the desired overview of the application. In our experience, such tools display repeated occurrences of non-trivial commun ...

12 Session 8: Floorplanning: An integrated floorplanning with an efficient buffer planning algorithm

Yuchun Ma, Xianlong Hong, Sheqin Dong, Song Chen, Yici Cai, C. K. Cheng, Jun Gu

April 2003 **Proceedings of the 2003 international symposium on Physical design**

Full text available:  [pdf\(213.79 KB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

Previous works on buffer planning are mainly based on fixed die placement. It is necessary to reduce the complexity of computing the feasible buffer insertion sites to integrate the buffer planning with the floorplanning process. In this paper, we give an efficient buffer planning algorithm with linear complexity by computing all the feasible buffer insertion sites in a 2-step method. By partitioning all the dead spaces into blocks while doing the packing, the buffer allocation can be handled as ...

Keywords: buffer insertion, floorplanning, routability

13 External memory algorithms

Jeffrey Scott Vitter

May 1998 **Proceedings of the seventeenth ACM SIGACT-SIGMOD-SIGART symposium on Principles of database systems**

Full text available:  [pdf\(1.68 MB\)](#)

Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

14 Distance browsing in spatial databases

Gísli R. Hjaltason, Hanan Samet

June 1999 **ACM Transactions on Database Systems (TODS)**, Volume 24 Issue 2

Full text available:  [pdf\(460.81 KB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

We compare two different techniques for browsing through a collection of spatial objects

stored in an R-tree spatial data structure on the basis of their distances from an arbitrary spatial query object. The conventional approach is one that makes use of a k-nearest neighbor algorithm where k is known prior to the invocation of the algorithm. Thus if $m < k$ neighbors are needed, the k-nearest neighbor alg ...

Keywords: R-trees, distance browsing, hierarchical spatial data structures, nearest neighbors, ranking

15 Comparison of access methods for time-evolving data

Betty Salzberg, Vassilis J. Tsotras

June 1999 **ACM Computing Surveys (CSUR)**, Volume 31 Issue 2

Full text available:  [pdf\(529.53 KB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

This paper compares different indexing techniques proposed for supporting efficient access to temporal data. The comparison is based on a collection of important performance criteria, including the space consumed, update processing, and query time for representative queries. The comparison is based on worst-case analysis, hence no assumptions on data distribution or query frequencies are made. When a number of methods have the same asymptotic worst-case behavior, features in the methods tha ...

Keywords: I/O performance, access methods, structures, temporal databases

16 A structural view of the Cedar programming environment

Daniel C. Swinehart, Polle T. Zellweger, Richard J. Beach, Robert B. Hagmann

August 1986 **ACM Transactions on Programming Languages and Systems (TOPLAS)**, Volume 8 Issue 4

Full text available:  [pdf\(6.32 MB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

This paper presents an overview of the Cedar programming environment, focusing on its overall structure—that is, the major components of Cedar and the way they are organized. Cedar supports the development of programs written in a single programming language, also called Cedar. Its primary purpose is to increase the productivity of programmers whose activities include experimental programming and the development of prototype software systems for a high-performance personal computer. Th ...

17 Efficient algorithms for local and global accessibility shading

Gavin Miller

July 1994 **Proceedings of the 21st annual conference on Computer graphics and interactive techniques**

Full text available:  [pdf\(1.11 MB\)](#)  [ps\(13.03 MB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

This paper discusses the use of two different approaches for computing the “accessibility” of a surface. These metrics characterize how easily a surface may be touched by a spherical probe. The paper also presents various acceleration techniques for accessibility. The idea of surface accessibility is extended to include “global accessibility” which measures the ability of a spherical probe to enter a structure from outside as well as to fit locally on the surface. Th ...

Keywords: aging, surface accessibility shading, visualisation

18 Geometric compression through topological surgery

Gabriel Taubin, Jarek Rossignac

April 1998 **ACM Transactions on Graphics (TOG)**, Volume 17 Issue 2

Full text available:  [pdf\(8.98 MB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

The abundance and importance of complex 3-D data bases in major industry segments, the affordability of interactive 3-D rendering for office and consumer use, and the exploitation

of the Internet to distribute and share 3-D data have intensified the need for an effective 3-D geometric compression technique that would significantly reduce the time required to transmit 3-D models over digital communication channels, and the amount of memory or disk space required to store the models. Because ...

Keywords: 3D mesh compression, VRML, geometry compression

19 Research sessions: moving objects: STRIPES: an efficient index for predicted trajectories



Jignesh M. Patel, Yun Chen, V. Prasad Chakka

June 2004 **Proceedings of the 2004 ACM SIGMOD international conference on Management of data**

Full text available: [pdf\(357.09 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#)

Moving object databases are required to support queries on a large number of continuously moving objects. A key requirement for indexing methods in this domain is to efficiently support both update and query operations. Previous work on indexing such databases can be broadly divided into categories: indexing the past positions and indexing the future predicted positions. In this paper we focus on an efficient indexing method for indexing the future positions of moving objects. In this paper we pr ...

20 Architecture of the IBM system/370



Richard P. Case, Andris Padegs

January 1978 **Communications of the ACM**, Volume 21 Issue 1

Full text available: [pdf\(2.78 MB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

This paper discusses the design considerations for the architectural extensions that distinguish System/370 from System/360. It comments on some experiences with the original objectives for System/360 and on the efforts to achieve them, and it describes the reasons and objectives for extending the architecture. It covers virtual storage, program control, data-manipulation instructions, timing facilities, multiprocessing, debugging and monitoring, error handling, and input/output operations. ...

Keywords: architecture, computer systems, error handling, instruction sets, virtual storage

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1 [Three-dimensional medical imaging: algorithms and computer systems](#)

M. R. Stytz, G. Frieder, O. Frieder

 December 1991 **ACM Computing Surveys (CSUR)**, Volume 23 Issue 4

 Full text available: [pdf\(7.38 MB\)](#) Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#), [review](#)


Keywords: Computer graphics, medical imaging, surface rendering, three-dimensional imaging, volume rendering

2 [External memory algorithms and data structures: dealing with](#)


massive data

Jeffrey Scott Vitter

 June 2001 **ACM Computing Surveys (CSUR)**, Volume 33 Issue 2

 Full text available: [pdf\(828.46 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

Data sets in large applications are often too massive to fit completely inside the computers internal memory. The resulting input/output communication (or I/O) between fast internal memory and slower external memory (such as disks) can be a major performance bottleneck. In this article we survey the state of the art in the design and analysis of external memory (or EM) algorithms and data structures, where the goal is to exploit locality in order to reduce the I/O costs. We consider a varie ...

Keywords: B-tree, I/O, batched, block, disk, dynamic, extendible hashing, external memory, hierarchical memory, multidimensional access methods, multilevel memory, online, out-of-core, secondary storage, sorting

3 [Array regrouping and structure splitting using whole-program reference affinity](#)


Yutao Zhong, Maksim Orlovich, Xipeng Shen, Chen Ding

 June 2004 **ACM SIGPLAN Notices, Proceedings of the ACM SIGPLAN 2004 conference on Programming language design and implementation**, Volume 39 Issue 6

 Full text available: [pdf\(202.16 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

While the memory of most machines is organized as a hierarchy, program data are laid out in a uniform address space. This paper defines a model of *reference affinity*, which measures how close a group of data are accessed together in a reference trace. It proves that the model gives a hierarchical partition of program data. At the top is the set of all data with the weakest affinity. At the bottom is each data element with the strongest

affinity. Based on the theoretical model, the paper p ...

Keywords: array regrouping, program locality, program transformation, reference affinity, reuse signature, structure splitting, volume distance

4 Fast detection of communication patterns in distributed executions 

Thomas Kunz, Michiel F. H. Seuren

November 1997 **Proceedings of the 1997 conference of the Centre for Advanced Studies on Collaborative research**

Full text available:  pdf(4.21 MB) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Understanding distributed applications is a tedious and difficult task. Visualizations based on process-time diagrams are often used to obtain a better understanding of the execution of the application. The visualization tool we use is Poet, an event tracer developed at the University of Waterloo. However, these diagrams are often very complex and do not provide the user with the desired overview of the application. In our experience, such tools display repeated occurrences of non-trivial commun ...

5 Three-dimensional object recognition 

Paul J. Besl, Ramesh C. Jain

March 1985 **ACM Computing Surveys (CSUR)**, Volume 17 Issue 1

Full text available:  pdf(7.76 MB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#), [review](#)

A general-purpose computer vision system must be capable of recognizing three-dimensional (3-D) objects. This paper proposes a precise definition of the 3-D object recognition problem, discusses basic concepts associated with this problem, and reviews the relevant literature. Because range images (or depth maps) are often used as sensor input instead of intensity images, techniques for obtaining, processing, and characterizing range data are also surveyed.

6 Register allocation across procedure and module boundaries 

Vatsa Santhanam, Daryl Odnert

June 1990 **ACM SIGPLAN Notices , Proceedings of the ACM SIGPLAN 1990 conference on Programming language design and implementation**, Volume 25 Issue 6

Full text available:  pdf(1.51 MB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

This paper describes a method for compiling programs using interprocedural register allocation. A strategy for handling programs built from multiple modules is presented, as well as algorithms for global variable promotion and register spill code motion. These algorithms attempt to address some of the shortcomings of previous interprocedural register allocation strategies. Results are given for an implementation on a single register file RISC-based architec ...

7 Optimal communication algorithms for regular decompositions on the hypercube 

G. C. Fox, W. Furtmanski

January 1988 **Proceedings of the third conference on Hypercube concurrent computers and applications: Architecture, software, computer systems, and general issues - Volume 1**

Full text available:  pdf(4.81 MB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

We discuss optimal communication and decomposition algorithms for a class of regular problems on concurrent computers with a hypercube topology, using a general technique we call the method of cube geodesics. We address the calculation of various transformations (convolutions, functionals etc.) of data distributed over the hypercube; examples are the Fast Fourier Transform, matrix algorithms, global scalar products and vector sums, sorting. These all involve long distance inter ...

8 Data and memory optimization techniques for embedded systems 

P. R. Panda, F. Catthoor, N. D. Dutt, K. Danckaert, E. Brockmeyer, C. Kulkarni, A.

We present a survey of the state-of-the-art techniques used in performing data and memory-related optimizations in embedded systems. The optimizations are targeted directly or indirectly at the memory subsystem, and impact one or more out of three important cost metrics: area, performance, and power dissipation of the resulting implementation. We first examine architecture-independent optimizations in the form of code transformations. We next cover a broad spectrum of optimizati ...

Keywords: DRAM, SRAM, address generation, allocation, architecture exploration, code transformation, data cache, data optimization, high-level synthesis, memory architecture customization, memory power dissipation, register file, size estimation, survey

9 Communication optimization and code generation for distributed memory machines

Saman P. Amarasinghe, Monica S. Lam

June 1993 **ACM SIGPLAN Notices , Proceedings of the ACM SIGPLAN 1993 conference on Programming language design and implementation**, Volume 28 Issue 6

Full text available:  pdf(1.56 MB)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)



This paper presents several algorithms to solve code generation and optimization problems specific to machines with distributed address spaces. Given a description of how the computation is to be partitioned across the processors in a machine, our algorithms produce an SPMD (single program multiple data) program to be run on each processor. Our compiler generated the necessary receive and send instructions, optimizes the communication by eliminating redundant communication and aggregating s ...

10 A Tutorial on Algol 68

Andrew S. Tanenbaum

June 1976 **ACM Computing Surveys (CSUR)**, Volume 8 Issue 2

Full text available:  pdf(2.92 MB)

Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)



11 Dynamic metrics for java

Bruno Dufour, Karel Driesen, Laurie Hendren, Clark Verbrugge

October 2003 **ACM SIGPLAN Notices , Proceedings of the 18th ACM SIGPLAN conference on Object-oriented programming, systems, languages, and applications**, Volume 38 Issue 11

Full text available:  pdf(222.67 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)



In order to perform meaningful experiments in optimizing compilation and run-time system design, researchers usually rely on a suite of benchmark programs of interest to the optimization technique under consideration. Programs are described as *numeric*, *memory-intensive*, *concurrent*, or *object-oriented*, based on a qualitative appraisal, in some cases with little justification. We believe it is beneficial to quantify the behaviour of programs with a concise and precisely ...

Keywords: Java, dynamic metrics, execution traces, optimization, profiling, program analysis, software metrics

12 Distributed file systems: concepts and examples

Eliezer Levy, Abraham Silberschatz

December 1990 **ACM Computing Surveys (CSUR)**, Volume 22 Issue 4

Full text available:  pdf(5.33 MB)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#), [review](#)



The purpose of a distributed file system (DFS) is to allow users of physically distributed computers to share data and storage resources by using a common file system. A typical configuration for a DFS is a collection of workstations and mainframes connected by a local area network (LAN). A DFS is implemented as part of the operating system of each of the connected computers. This paper establishes a viewpoint that emphasizes the dispersed structure and decentralization of both data and con ...

13 Extending high performance Fortran for the support of unstructured computations

Andreas Müller, Roland Rühl

July 1995 **Proceedings of the 9th international conference on Supercomputing**

Full text available:  [pdf\(1.33 MB\)](#) Additional Information: [full citation](#), [references](#), [index terms](#)



14 Technical reports

SIGACT News Staff

January 1980 **ACM SIGACT News**, Volume 12 Issue 1

Full text available:  [pdf\(5.28 MB\)](#) Additional Information: [full citation](#)



15 Scalable high-speed prefix matching

Marcel Waldvogel, George Varghese, Jon Turner, Bernhard Plattner

November 2001 **ACM Transactions on Computer Systems (TOCS)**, Volume 19 Issue 4

Full text available:  [pdf\(933.02 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)



Finding the longest matching prefix from a database of keywords is an old problem with a number of applications, ranging from dictionary searches to advanced memory management to computational geometry. But perhaps today's most frequent best matching prefix lookups occur in the Internet, when forwarding packets from router to router. Internet traffic volume and link speeds are rapidly increasing; at the same time, a growing user population is increasing the size of routing tables against which p ...

Keywords: collision resolution, forwarding lookups, high-speed networking

16 Scalable concurrent priority queue algorithms

Nir Shavit, Asaph Zemach

May 1999 **Proceedings of the eighteenth annual ACM symposium on Principles of distributed computing**

Full text available:  [pdf\(1.35 MB\)](#) Additional Information: [full citation](#), [references](#), [index terms](#)



17 Transitive closure algorithms based on graph traversal

Yannis Ioannidis, Raghu Ramakrishnan, Linda Winger

September 1993 **ACM Transactions on Database Systems (TODS)**, Volume 18 Issue 3

Full text available:  [pdf\(4.34 MB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)



Several graph-based algorithms have been proposed in the literature to compute the transitive closure of a directed graph. We develop two new algorithms (Basic_TC and Gobal_DFTC) and compare the performance of their implementations in a disk-based environment with a well-known graph-based algorithm proposed by Schmitz. Our algorithms use depth-first search to traverse a graph and a technique called marking to avoid processing some of the arcs in the graph. They compute the ...

Keywords: depth-first search, node reachability, path computations, transitive closure

18

Verification of microprogrammed computer architectures in the S*-system: a case



study

W. Damm, G. Dohmen

December 1985 **ACM SIGMICRO Newsletter , Proceedings of the 18th annual workshop on Microprogramming**, Volume 16 Issue 4

Full text available:  [pdf\(1.43 MB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

We apply the verification methodology underlying the S*-System[12], [13] to the verification of a hierarchically structured design [16] of an emulation of the instruction-set of a commercially available computer on a commercially available micro-architecture. Based on this case-study, we discuss some aspects of the relation between verification and generation of microcode.



19 Computational strategies for object recognition

Paul Suetens, Pascal Fua, Andrew J. Hanson

March 1992 **ACM Computing Surveys (CSUR)**, Volume 24 Issue 1

Full text available:  [pdf\(6.37 MB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#), [review](#)

This article reviews the available methods for automated identification of objects in digital images. The techniques are classified into groups according to the nature of the computational strategy used. Four classes are proposed: (1) the simplest strategies, which work on data appropriate for feature vector classification, (2) methods that match models to symbolic data structures for situations involving reliable data and complex models, (3) approaches that fit models to the photometry and ...

Keywords: image understanding, model-based vision, object recognition



20 Parallelism in relational data base systems: architectural issues and design

approaches

Hamid Pirahesh, C. Mohan, Josephine Cheng, T. S. Liu, Pat Selinger

July 1990 **Proceedings of the second international symposium on Databases in parallel and distributed systems**

Full text available:  [pdf\(2.50 MB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

With current systems, some important complex queries may take days to complete because of: (1) the volume of data to be processed, (2) limited aggregate resources. Introducing parallelism addresses the first problem. Cheaper, but powerful computing resources solve the second problem. According to a survey by Brodie, only 10% of computerized data is in data bases. This is an argument for both more variety and volume of data to be moved into data base systems. We conject ...

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1 Expanded rectangles: a new VLSI data structure

Quayle, M.; Solworth, J.;
Computer-Aided Design, 1988. ICCAD-88. Digest of Technical Papers., IEEE International Conference on, 7-10 Nov. 1988
Pages:538 - 541

[\[Abstract\]](#) [\[PDF Full-Text \(304 KB\)\]](#) **IEEE CNF**

2 Supervision of a steel strip rinsing process

Sohlberg, B.;
Decision and Control, 1992., Proceedings of the 31st IEEE Conference on, 16-18 Dec. 1992
Pages:2557 - 2561 vol.3

[\[Abstract\]](#) [\[PDF Full-Text \(483 KB\)\]](#) **IEEE CNF**

3 On the neural defuzzification methods

Halgamuge, S.K.; Runkler, T.A.; Glesner, M.;
Fuzzy Systems, 1996., Proceedings of the Fifth IEEE International Conference on, Volume: 1, 8-11 Sept. 1996
Pages:463 - 469 vol.1

[\[Abstract\]](#) [\[PDF Full-Text \(744 KB\)\]](#) **IEEE CNF**

4 A proactive implementation of interactive video-on-demand

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Performance, Computing, and Communications Conference, 2003. Conference Proceedings of the 2003 IEEE International, 9-11 April 2003
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